

Philadelphia University Faculty of Engineering Department of Computer Engineering First Semester, 2015/2016

Course Syllabus

Course Title: REAL-TIME SYSTEMS	Course code: 630512
Course Level: 5 th Year	Course prerequisite(s): 630430
Class Time: 9:10-11:10 Sun,Tue,Thu	Credit hours: 3

	<u>Acade</u>	emic Staff		
	<u>Sp</u>	<u>ecifics</u>		
Name	Rank	Office No.	Office Hours	E-mail Address
Dr. Mohammed Mahdi Ali	Assistant Prof.	709	8:10-9:00 Weekly	M_selman@philadelphia.edu.jo

Course description:

It provides the basic introduction to real-time systems design, development and implementation. It includes; hardware design of input/output interface between a microcomputer or a microcontroller and a plant, real-time algorithms design and realization, stability of microcomputer-based systems, real-time operating systems, and other related topics.

Course objectives:

The main objective of this course is to cover the principles and design methods of real-time computer systems. It covers the interfacing techniques and microprocessor system realization. The principles of real-time operating systems and real-time software system will be covered in this course.

Course components

- Books (title, author (s), publisher, year of publication)
 - Real-Time Computer Control, By: Stuart Bennett, Prentice-Hall, 2nd edition, 1994.
- Support material (s) (vcs, acs, etc).
- Study guide (s) (if applicable)
- Homework and laboratory guide (s) if (applicable).

Teaching methods:

Lectures, discussion groups, tutorials, problem solving, debates, etc.

Learning outcomes: upon completing this course, the student should have: -

• Knowledge and understanding: Understanding principles of embedded systems design; be aware of architectures and behaviors of embedded systems.

	Course Intended Learning Outcomes											
A - Knowledge and Understanding												
A1.	A2.		A3.	A	4.	A:	5.	A	5.	A7.		A8.
B - Inte	llectual S	Skills										
B1.	B2.	В3	3.	B4.	В	5.	В6		B7.	B8.		B9.
C - Prac	C - Practical Skills											
C1.	C2.	C3.	C ²	4. (C5.	C6		C7.	C8	. C9	١.	C10.
D - Transferable Skills												
D1.	I	02.	Г	D 3.	D	4.		D5.		D6.		D7.
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Assessment instruments

- **Short reports and presentations**: Reading related to current topic will be assigned every week. Assignments and other Homework (HW) will be given throughout the semester, focusing on the concepts learned from these readings.
- Quizzes: TWO to THREE Quizzes will be offered (dates TBD).
- **Project:** Project is an essential part of this course. Assessment will be based on 3 phases: System Specification, System Design, Hardware and Software Implementation with Project Demonstration. Detailed topics and schedule will be announced in due course.
- **Final examination**: 40 marks

Allocation of Marks				
Assessment Instruments	Mark			
1 st examination	20%			
2 nd examination	20%			
Project & Presentation	10%			
Quizzes and Homework,	10%			
Final Examination:	40%			
Total	100%			

Documentation and academic honesty

- Documentation style (with illustrative examples)
- Protection by copyright
- Avoiding plagiarism.

• Ethics and Disability Act:

- Students may consult with one another on solutions, but copying another student's code is strictly prohibited.
- Students should write their own code. Using code found on books or internet is prohibited.
- The Instructor follows general university "Academic Dishonesty/Cheating Policy".

Course academic calendar

Course academic cal	Basic and support material to be	Hamayyank/nan	
1-	Homework/rep		
week	orts and their		
		due dates	
(1)	An introduction to real-time computer		
(-)	systems.		
	Elements of a real-time microcontroller-		
(2)	based system, Classification of RTS,		
(2)	Time constraints,		
	Classification of programs		
	Computer Control concepts; Sequence		
(3)	control, DDC, PID control, Adaptive		
. ,	control, Supervisory control,		
	Centralized control, Hierarchical	HW1	
(4)	systems, Distributed systems, Human-		
,	computer interface.		
	Hardware requirements for real-time	Project Selection	
(5)	systems: Analog I/O interfacing.	1 st exam	
(-)		18-26\11\2015	
(6)	Digital and Pulse I/O iterfacing.	10 20 11 12010	
	Data Acquisition and Data Distribution	HW2	
(7)	system design.	11,1,2	
(8)	Implementation of real-time algorithms,		
Mid Exam	ampromentum or rear time ungertimes,		
	Realization of real-time algorithms using		
(9)	single processors or more.		
	Stability analysis of real-time systems.	HW3	
		2 nd exam	
(10)		27\12\2015-	
		5\1\2016	
(11)	Software design of real-time systems.	Project (Phase1)	
	Operating systems for real-time	- J (= 1)	
(12)	applications: Basic features of RTOSs,		
(13)	Scheduling: concepts & implementation.	Project (Phase2)	
(14)	Languages for real-time applications.	- J (=3 -2)	
(15)	MiniProjects using Microcontrollers.		
Specimen Exam	.,		
(Optional)			
(16)		Final exam	
Final Exam		30\1-7\2\2016	
		1 1 1	

Expected workload:

On average students need to spend 2 hours of study and preparation for each 50-minute class/tutorial.

Attendance policy:

Absence from classes and/or tutorials shall not exceed 15%. Students who exceed the 15% limit without a medical or emergency excuse acceptable to and approved by the Dean of the relevant college/faculty shall not be allowed to take the final examination and shall receive a mark of zero for the course. If the excuse is approved by the Dean, the student shall be considered to have withdrawn from the course.

The student is responsible for all assignments on a weekly basis.

No make-up will be given for missed quizzes, tests or assignments, unless a case is made in advance with Instructor's approval.

Course references

Books

- 1. J. Cooling, Software Engineering for Real-Time Systems, Addison Wesley, UK 2003. www.pearsopneduc.com
- 2. J.W.S. LIN, Real-Time Systems, Prentice Hall, 2000.
- 3. N. NISSANKE, Real-Time Systems, Prentice Hall, 1997.
- 4. R.J.A. BUHR & D.L. BAILEY, An Introduction to Real-Time Systems, Prentice Hall,
- 5. S. BENNETT & G.S. VIRK, Computer Control of Real-Time Processes, IEE 1990.
- 6. J. Cooling, Software Engineering for Real-Time Systems, Addison Wesley, UK 2003.
- 7. W. VALVANO, Embedded Microcomputer Systems: Real-Time Interfacing, Brooks-Cole Publisher, 2000

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